

FIG. 1

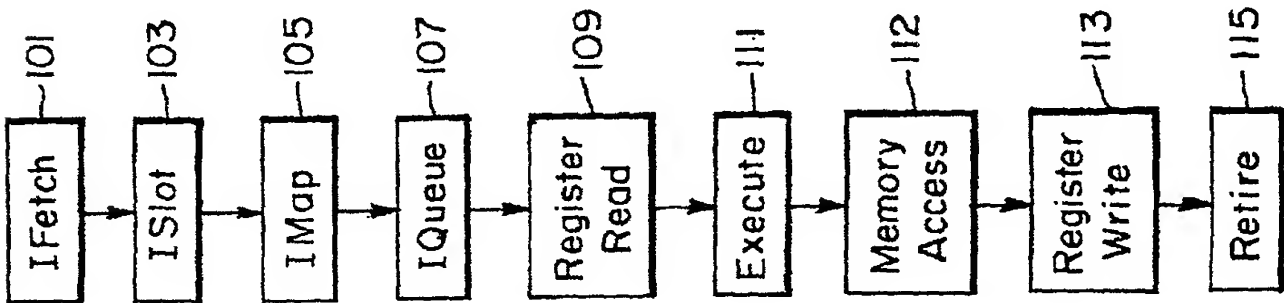


FIG. 1

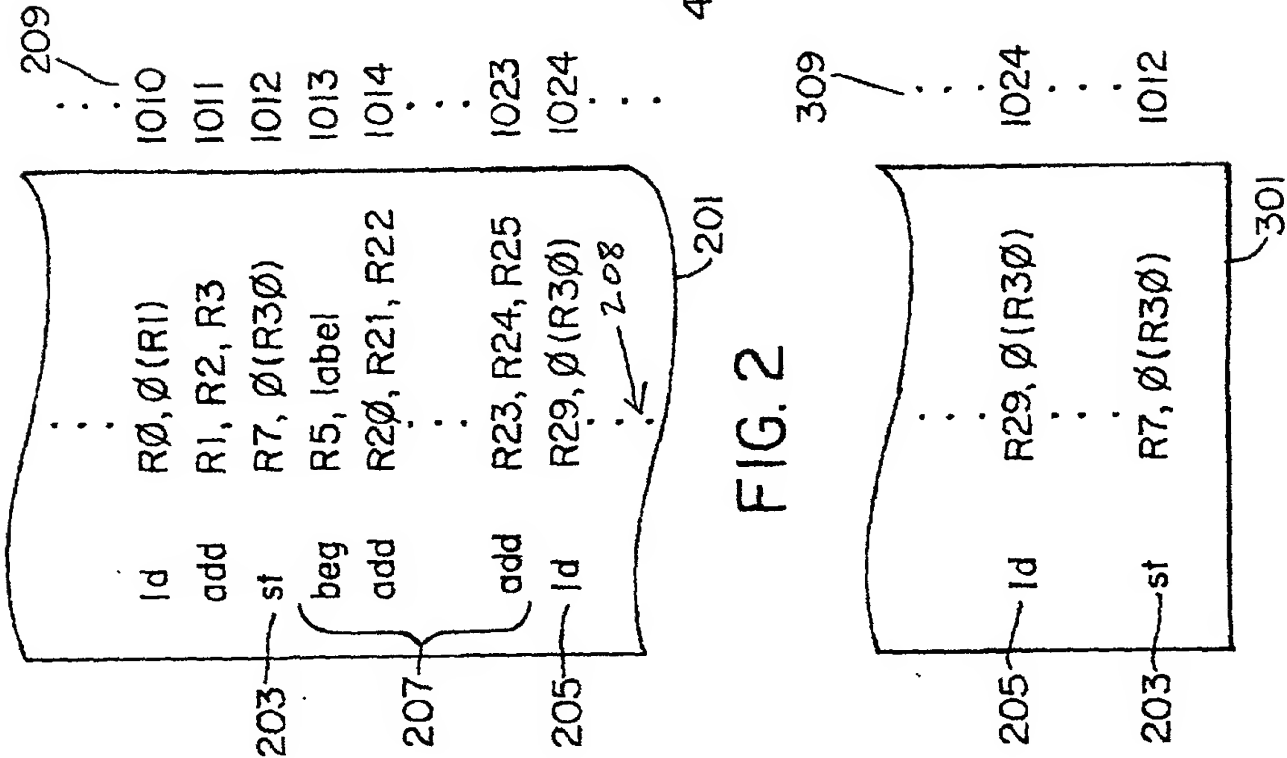


FIG. 2

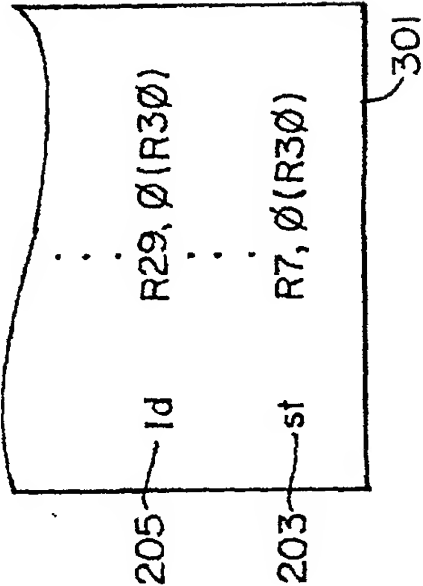


FIG. 3

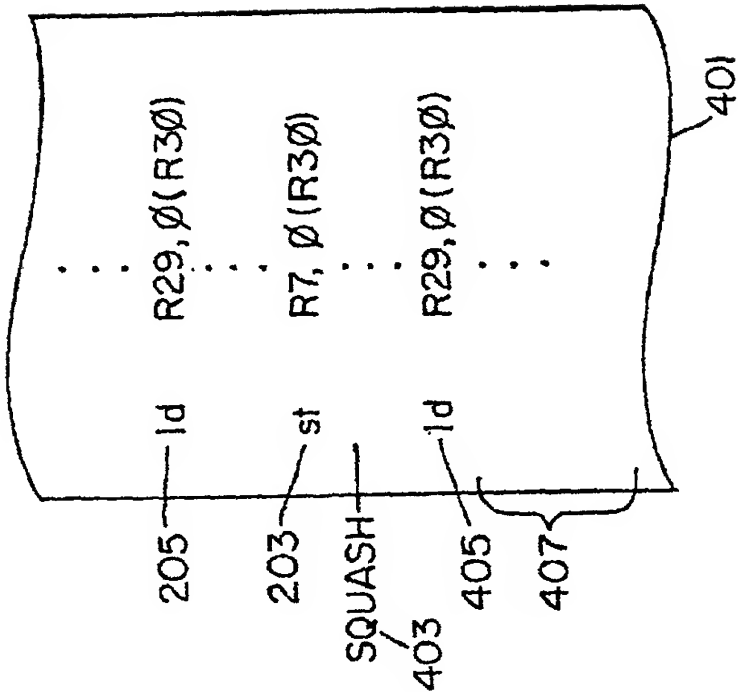
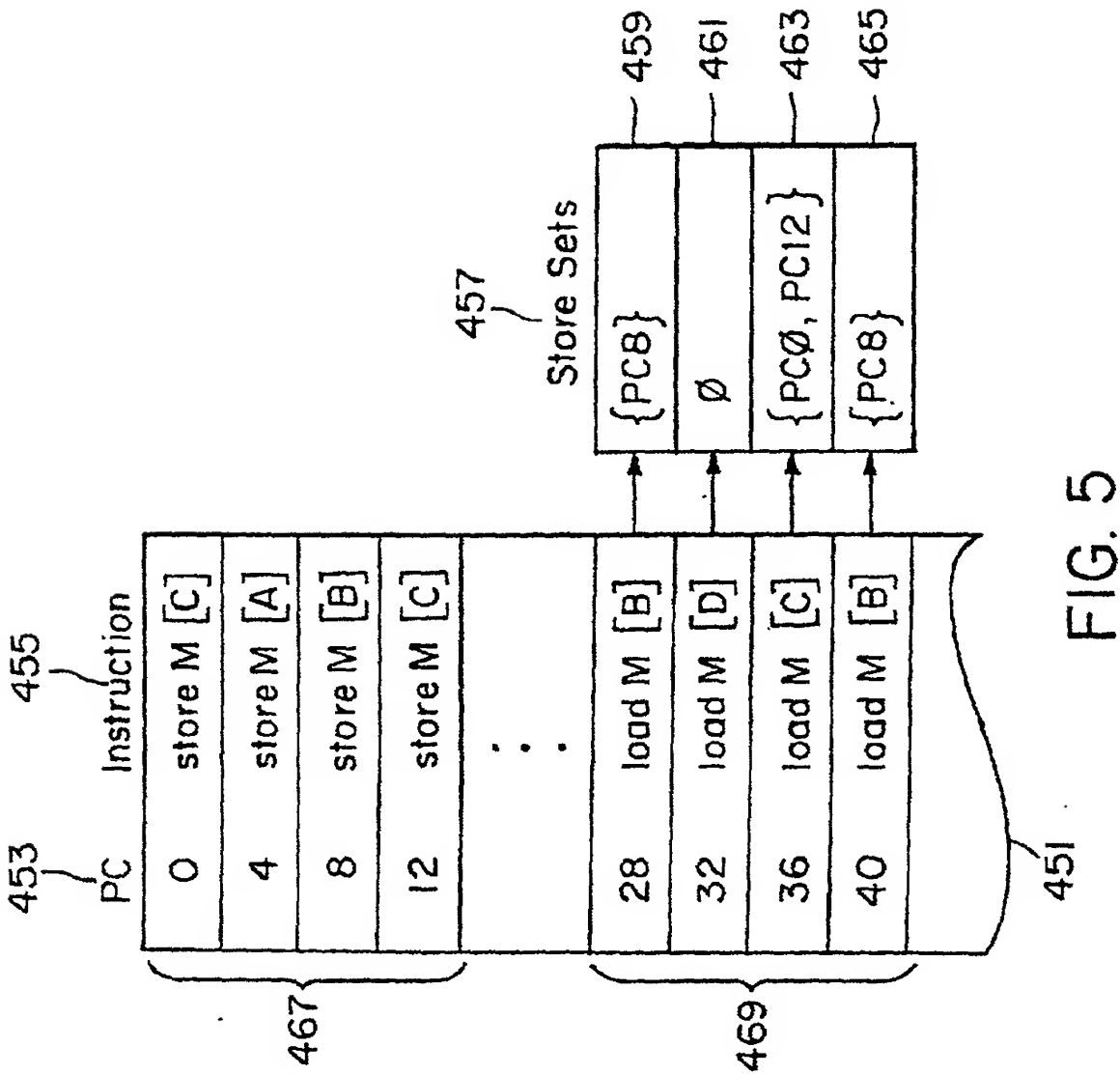


FIG. 4



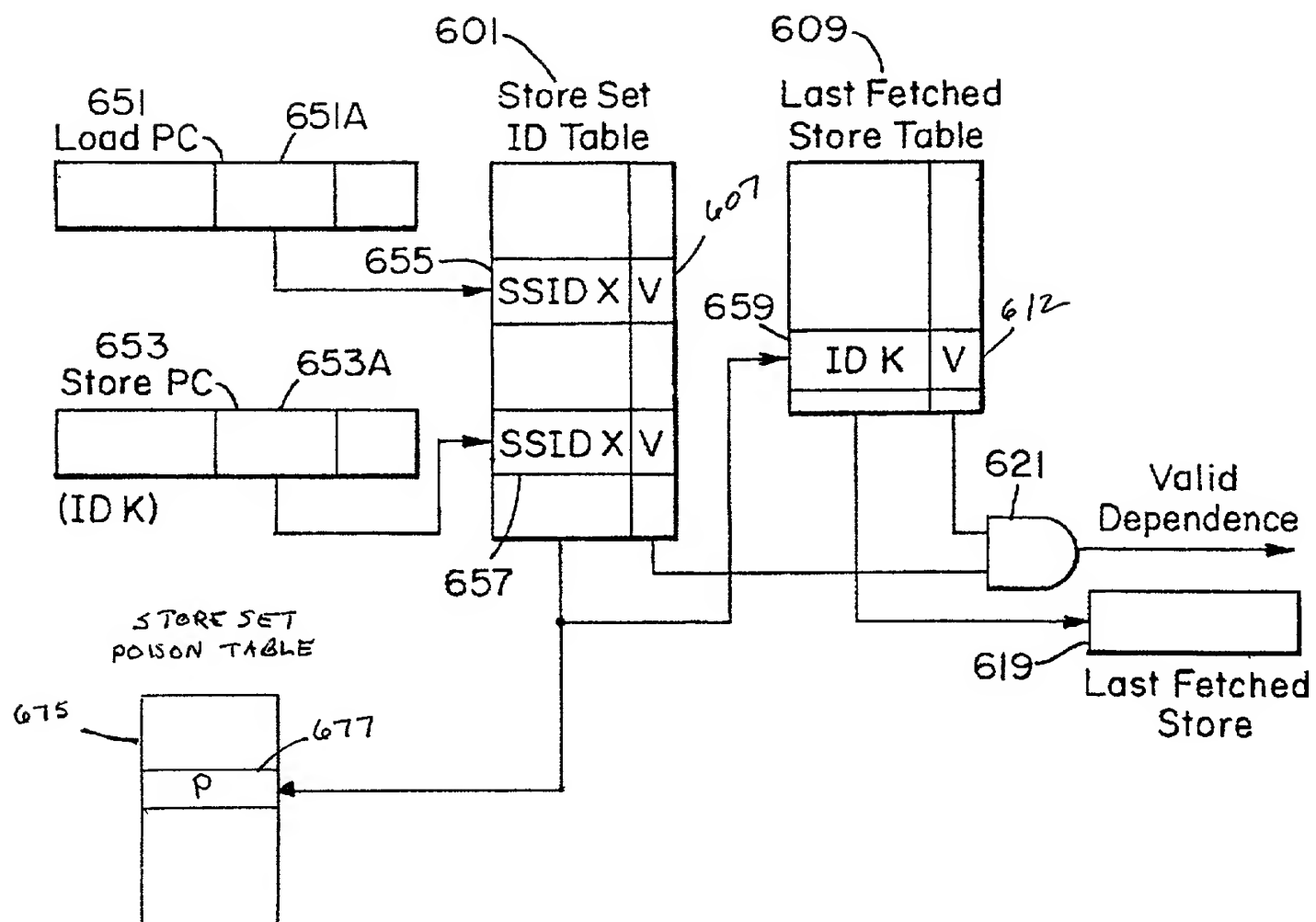


FIG. 6

$$\begin{aligned} \text{LOAD1} &= \text{LD mem}(y) \rightarrow R1 \\ &\vdots \\ \text{STORE1} &= \text{ST } R1 \rightarrow \text{mem}(x) \\ &\vdots \\ \text{LOAD2} &= \text{LD mem}(x) \rightarrow R3 \end{aligned}$$

FIG. 7

[illegible]

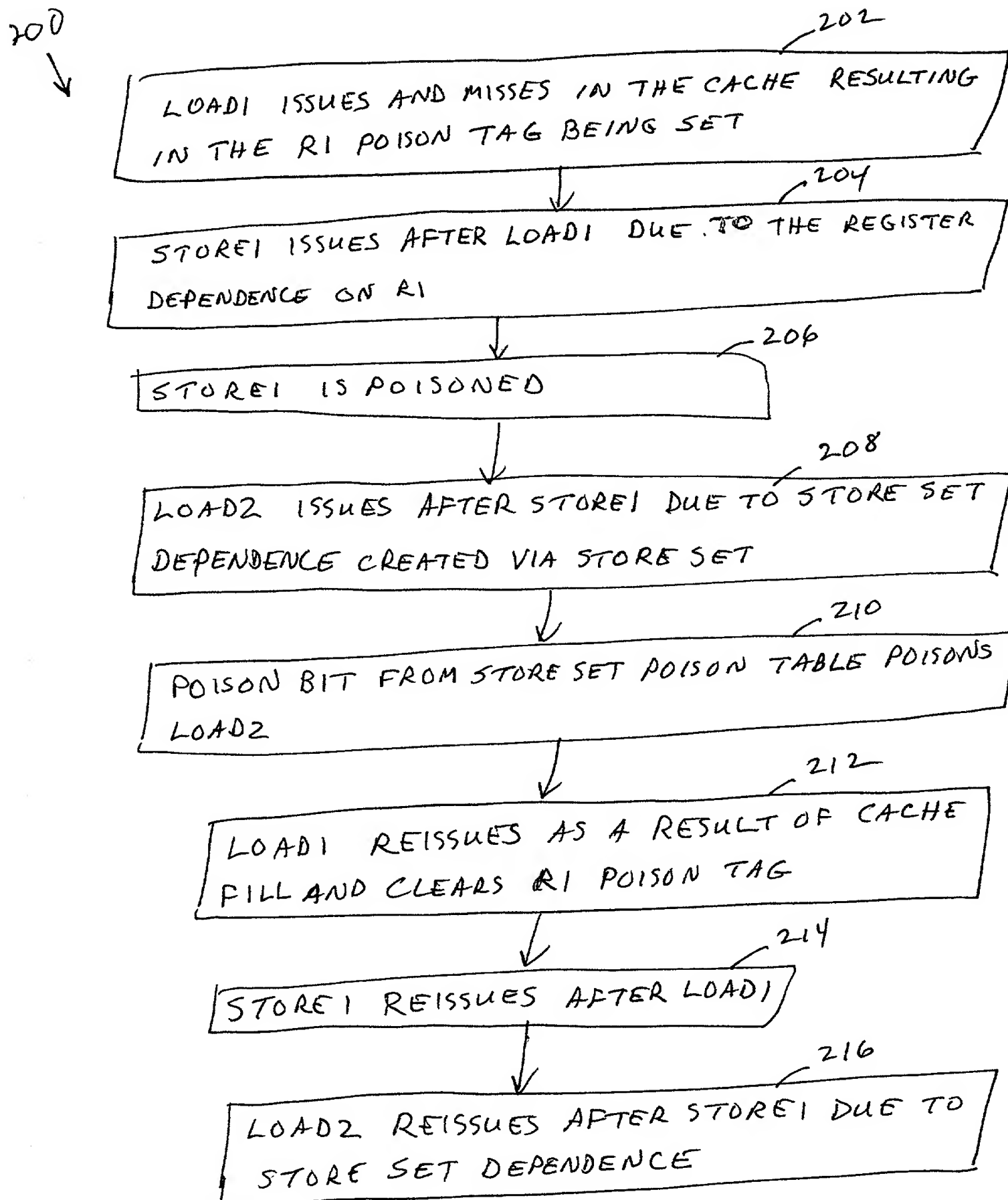


FIG. 8